

Electronic Patent Application Fee Transmittal

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|---|---|-----------------|---------------|-----------------------------|
| Application Number: | 10561627 | | | |
| Filing Date: | 19-Dec-2005 | | | |
| Title of Invention: | REGISTER FILE GATING TO REDUCE MICROPROCESSOR POWER DISSIPATION | | | |
| First Named Inventor/Applicant Name: | Andrei Terechko | | | |
| Filer: | Peter Zawilski | | | |
| Attorney Docket Number: | NL02 1504 US | | | |
| Filed as Large Entity | | | | |
| U.S. National Stage under 35 USC 371 Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Utility Appl issue fee | 1501 | 1 | 1510 | 1510 |
| Publ. Fee- early, voluntary, or normal | 1504 | 1 | 300 | 300 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|--------------------|----------|----------|--------|----------------------|
| Extension-of-Time: | | | | |
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 1810 |